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		Application / Conf. No.	09/684,529 / 1502
TRANSMITT	AL	Filing Date	October 06, 2000
FORM		First Named Inventor	John D. Logue
(to be used for all correspondence afte	initial filing)	Examiner Name	Eva Y. Zheng
Mail Stop: AF		Art Unit	PECEIVE 2634
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X-735 US 09/684,529

PATENT Conf. No. 1502

IN THE UNITED STATES PATENT OFFICE

🎖 plicants: John D. Logue, et al.

Assignee: Xilinx, Inc.

Title: Synchronized Multi-Output Digital Clock Manager

Serial No: 09/684,529 Filed: October 6, 2000

Examiner: Eva Y. Zheng Art Unit: 2634

Docket No: X-735 US Conf. No.: 1502

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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION Technology Center 2600

Dear Sir:

Applicants submit the following amendment in response to the Final Office Action dated May 26, 2004.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.